

• General Description

The ZMC88302S combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. It combine one N Channel MOSFET and one P channel MOSFET.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Dual DIE in one package

• Application

- Power Management in Notebook Computer
- BLDC Motor driver

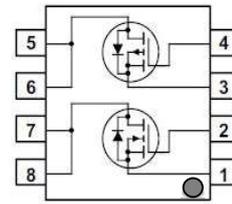
• Ordering Information:

Part NO.	ZMC88302S
Marking	ZMC88302
Packing Information	REEL TAPE
Basic ordering unit (pcs)	4000

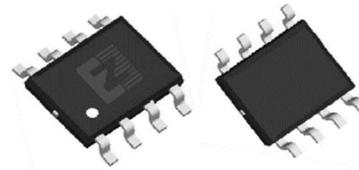
• N Channel Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_{D@TC=25^\circ\text{C}}$	9	A
	$I_{D@TC=75^\circ\text{C}}$	6.8	A
	$I_{D@TC=100^\circ\text{C}}$	5.7	A
Pulsed Drain Current ^①	I_{DM}	20	A
Total Power Dissipation($TC=25^\circ\text{C}$)	$P_D@TC=25^\circ\text{C}$	3.6	W
Total Power Dissipation($TA=25^\circ\text{C}$)	$P_D@TA=25^\circ\text{C}$	0.69	W
Operating Junction Temperature	T_J	-55 to 150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^\circ\text{C}$
Single Pulse Avalanche Energy	E_{AS}	45	mJ

• Product Summary



$V_{DS1} = 30\text{V}$
 $V_{DS2} = -30\text{V}$
 $R_{DS(ON)1} = 12\text{m}\Omega$
 $R_{DS(ON)2} = 18\text{m}\Omega$
 $I_{D1} = 9\text{A}$
 $I_{D2} = -7\text{A}$



SOP8

•P Channel Absolute Maximum Ratings (T_c =25°C)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _{D@TC=25°C}	-7	A
	I _{D@TC=75°C}	-5.3	A
	I _{D@TC=100°C}	-4.4	A
Pulsed Drain Current ^①	I _{DM}	-16	A
Total Power Dissipation(TC=25°C)	P _{D@TC=25°C}	3.6	W
Total Power Dissipation(TA=25°C)	P _{D@TA=25°C}	0.69	W
Operating Junction Temperature	T _J	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C
Single Pulse Avalanche Energy	E _{AS}	65	mJ

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	34	° C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	180	° C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	° C

•N Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =6A		12	15	mΩ
		V _{GS} =4.5V, I _D =4A		19	22	mΩ
Forward Transconductance	g _{FS}	V _{DS} =25V, I _D =5A		8		s
Source-drain voltage	V _{SD}	I _s =6A			1.28	V

**•Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C_{iss}	V _{DS} =25V f = 1MHz	-	560	-	pF
Output capacitance	C_{oss}		-	81	-	
Reverse transfer capacitance	C_{rss}		-	49	-	

•Gate Charge characteristics(T_a = 25°C)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q_g	V _{DD} = 15V	-	10	-	nC
Gate - Source charge	Q_{gs}	I _D = 6A	-	1.6	-	
Gate - Drain charge	Q_{gd}	V _{GS} = 10V	-	2.8	-	

•P Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = -250uA	-30			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = -250uA	-1.2		-2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V			-1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -6A		18	22	mΩ
		V _{GS} = -4.5V, I _D = -4A		30	35	mΩ
Forward Transconductance	g _{FS}	V _{DS} = -10V, I _D = -5A		11		s
Source-drain voltage	V _{SD}	I _S = -6A			1.28	V

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C_{iss}	V _{DS} = -25V f = 1MHz	-	1400	-	pF
Output capacitance	C_{oss}		-	125	-	
Reverse transfer capacitance	C_{rss}		-	110	-	

•Gate Charge characteristics(T_a = 25°C)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q_g	V _{DD} = -15V	-	26.1	-	nC
Gate - Source charge	Q_{gs}	I _D = -6A	-	4.9	-	
Gate - Drain charge	Q_{gd}	V _{GS} = -10V	-	4	-	

•N Channel characteristics curve

Fig.1 Power Dissipation

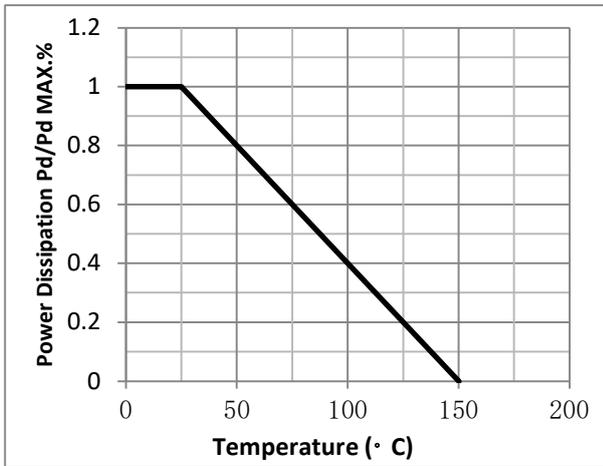


Fig.2 Typical output Characteristics

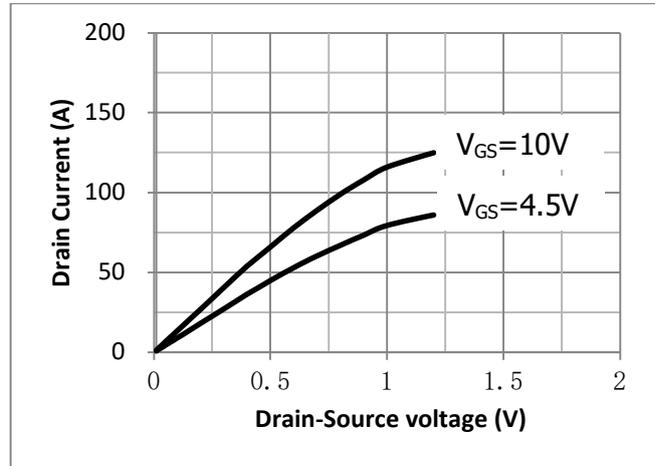


Fig.3 Threshold Voltage V.S Junction Temperature

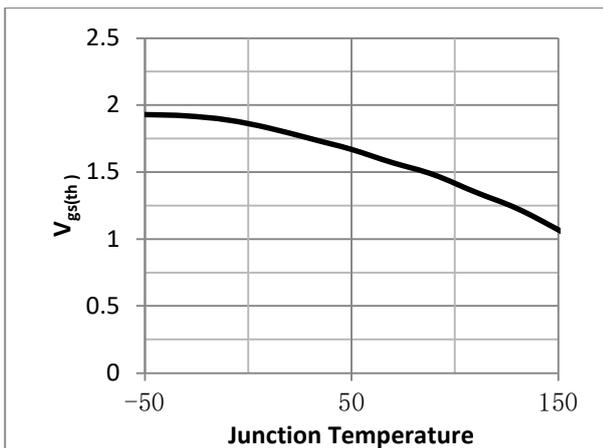


Fig.4 Resistance V.S Drain Current

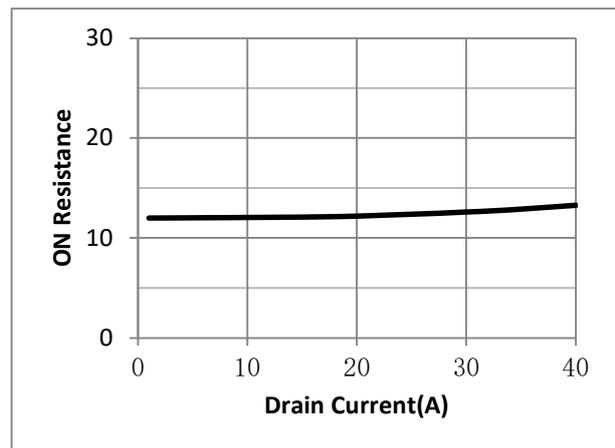


Fig.5 On-Resistance VS Gate Source Voltage

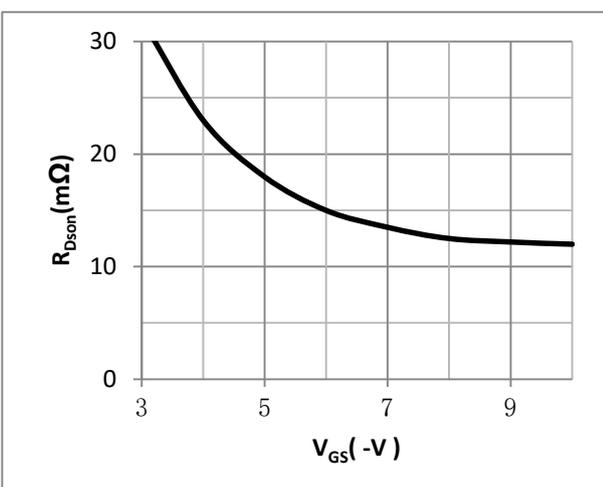
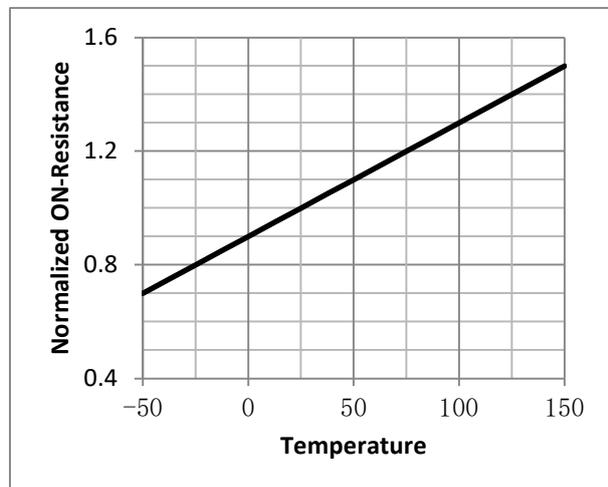


Fig.6 On-Resistance V.S Junction Temperature



•P Channel characteristics curve

Fig.1 Power Dissipation Derating Curve

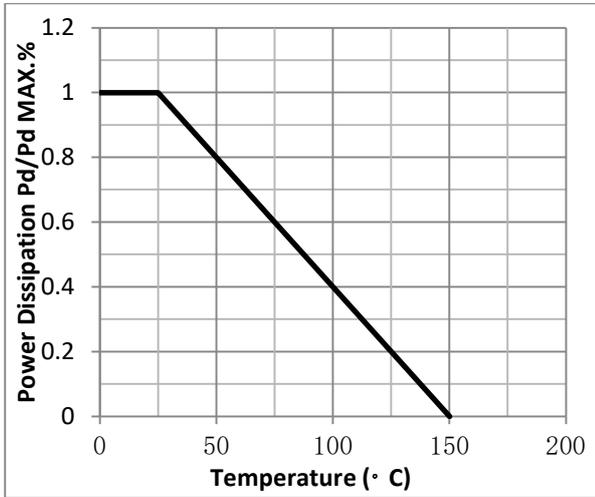


Fig.2 Typical output Characteristics

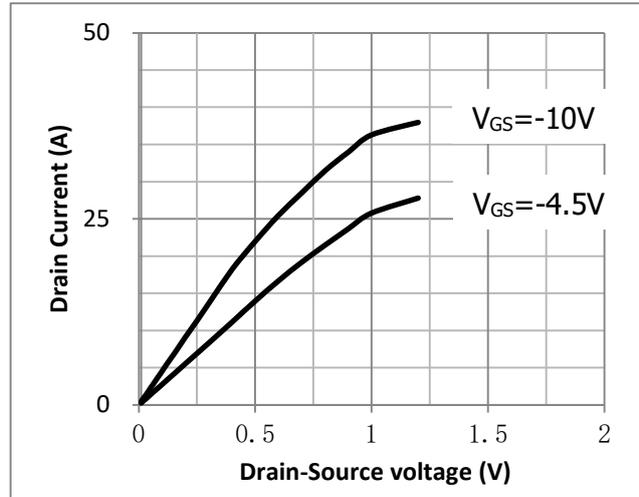


Fig.3 Threshold Voltage V.S Junction Temperature

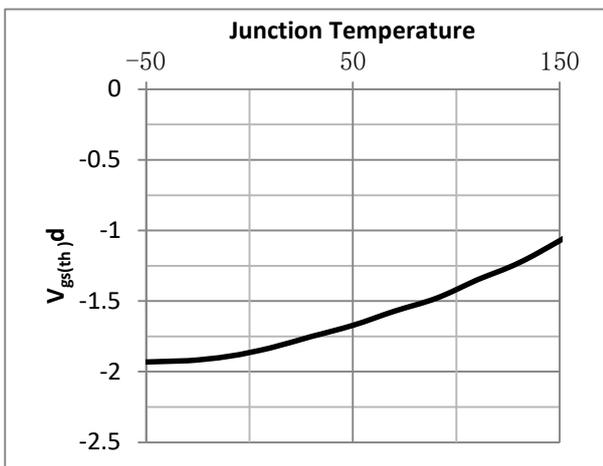


Fig.4 Resistance V.S Drain Current

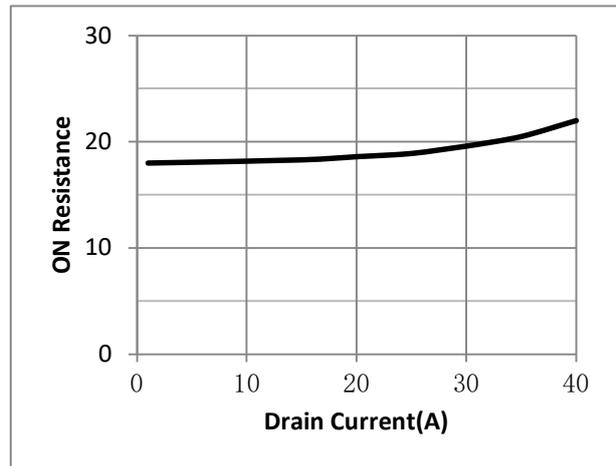


Fig.5 On-Resistance VS Gate Source Voltage

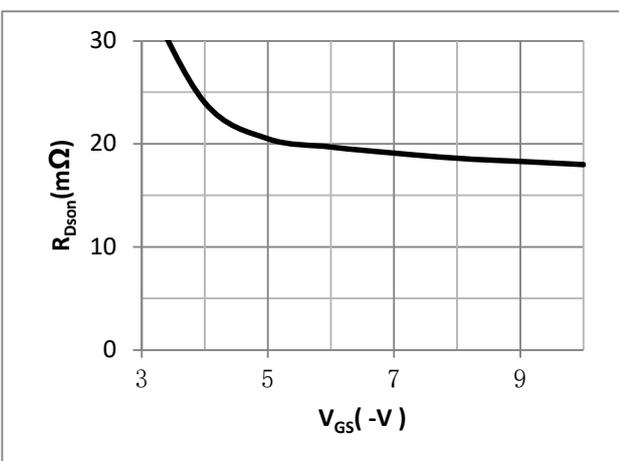
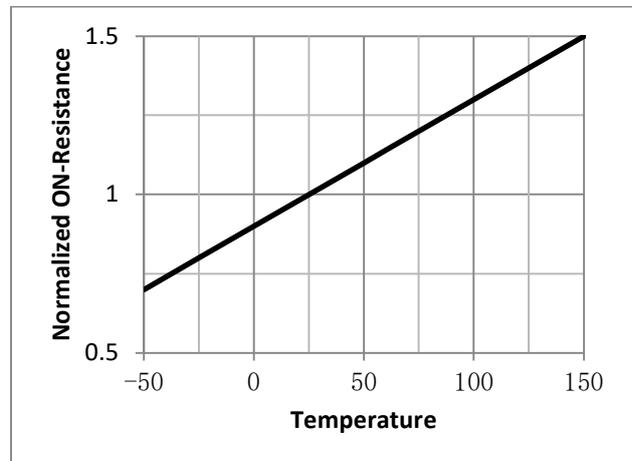


Fig.6 On-Resistance V.S Junction Temperature



•Test Circuit CHANNEL-N

Fig.1 Gate Charge Measurement Circuit

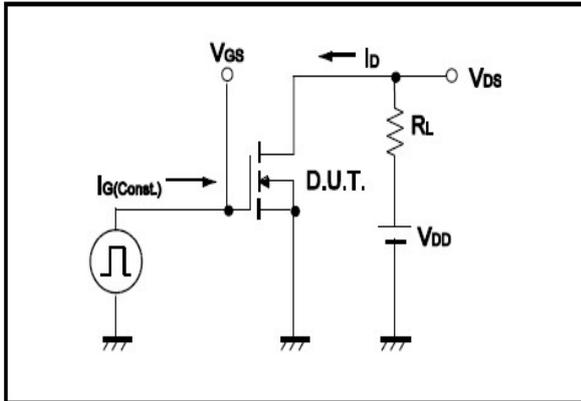


Fig.2 Gate Charge Waveform

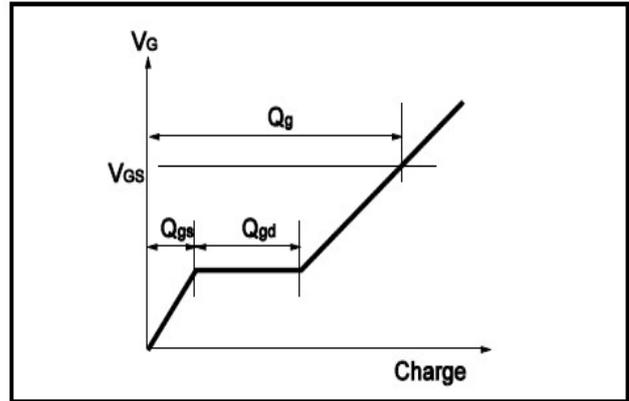


Fig.3 Switching Time Measurement Circuit

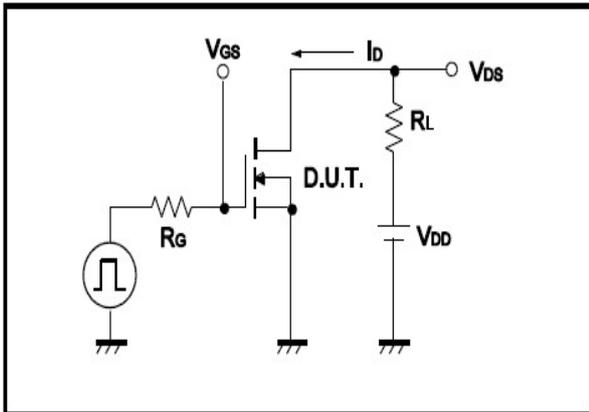


Fig.4 Switching Time Waveform

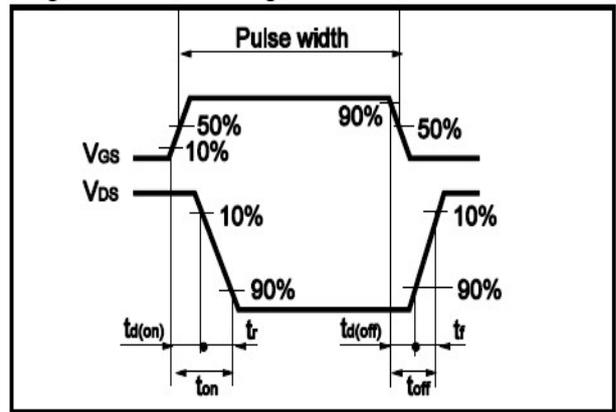


Fig.5 Avalanche Measurement Circuit

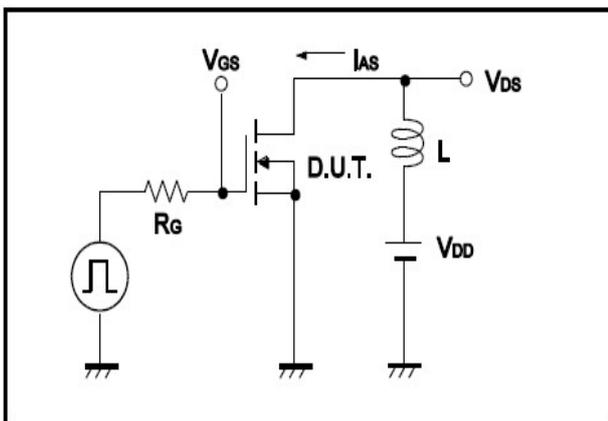
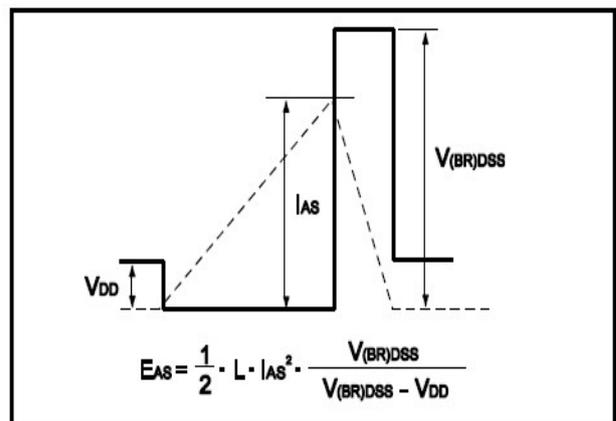


Fig.6 Avalanche Waveform



•Test Circuit CHANNEL-P

Fig.7 Gate Charge Measurement Circuit

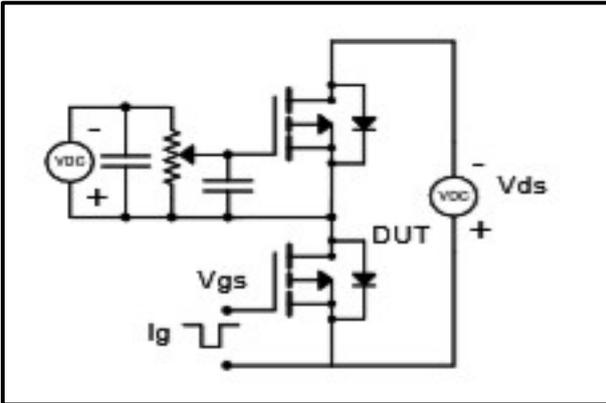


Fig.8 Gate Charge Waveform

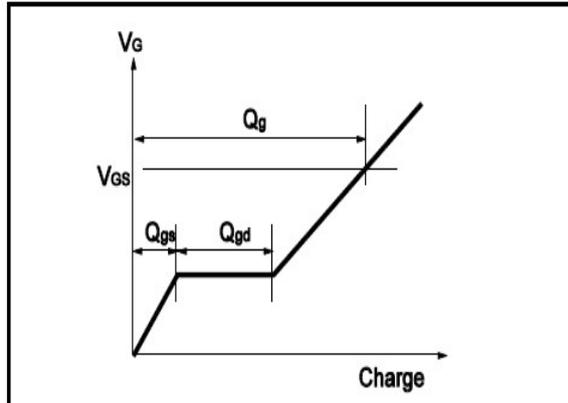


Fig.9 Switching Time Measurement Circuit

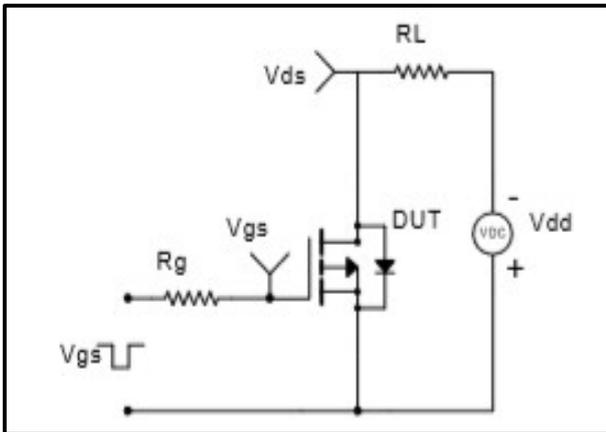


Fig.10 Switching Time Waveform

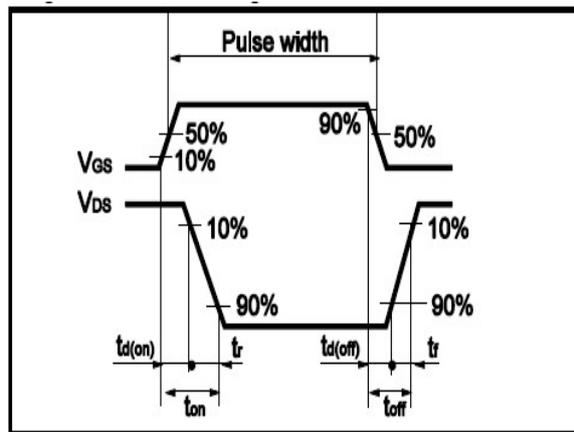


Fig.11 Avalanche Measurement Circuit

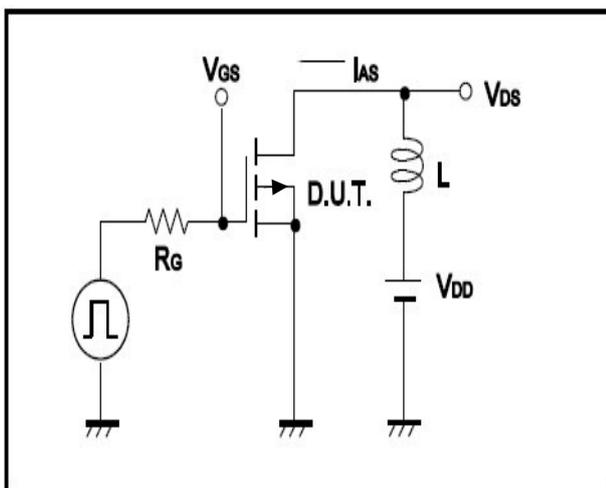
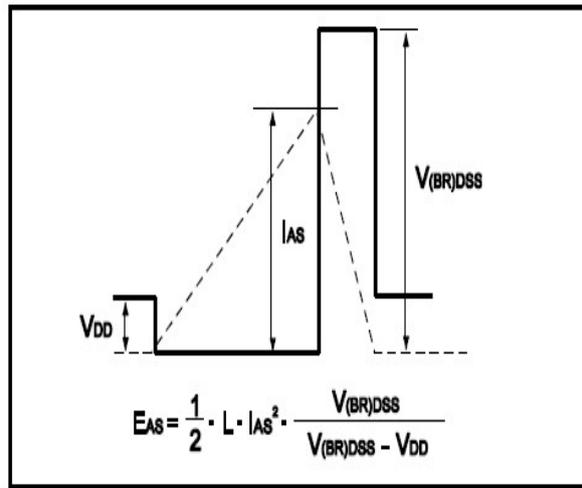


Fig.12 Avalanche Waveform



●Dimensions(SOP8)

Unit: mm

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.80		5.00	C	1.30		1.50
A1	0.37		0.47	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.20
B	5.80		6.20	C4	0.19	0.20	0.23
B1	3.80		4.00	D		1.05	
B2		5.00		D1	0.40		0.62

